#### **MIF Status Report**

#### Yuta Michimura

Department of Physics, University of Tokyo

on behalf of the <u>Main Interferometer subgroup</u>

# Tasks for MIF Subgroup

- Design the Main Interferometer including layout, controls scheme, optics ...  $\rightarrow$  DONE
- Derive noise requirements for suspensions, electronics, optics  $\rightarrow$  mostly DONE
- Instrumentation of detection optics including optics, photodiodes, cabling, electronics → ON GOING
- Interferometer commissioning including initial alignment, mode-matching, integration of signals to DAQ system, etc.

ITMY

# Tasks for MIF Subgroup

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- Derive noise requirements for suspensions, electronics, optics  $\rightarrow$  mostly DONE
- Instrumentation of detection optics including optics, photodiodes, cabling, electronics → ON GOING
- It seems like EO people think commissioning tasks are for "commissioning team" lead by ON-SITE people

ITMY

# MIF Members FY2016 ON SITE OFF SITE

Effort Affiliation **Actual Contribution So Far** Name Yuta Michimura UTokyo Chief, Management, Electronics 30 Yoichi Aso Sub-chief, Management NAOJ 10 Osamu Miyakawa **ICRR** 10 Keiko Kokeyama ICRR 10 Commissioning, Cameras Eiichi Hirose ICRR 10 Mirror simulation Kentaro Somiya OMC, OFI Titech 10 Tomotada Akutsu NAOJ 5 AOS related UTokyo Kentaro Komori 5 UTokyo 5 Naoki Aritomi UTokyo Magnetic noise, suspension Tomofumi Shimoda 5 **ICRR** 5 Yutaro Enomoto Interferometer simulation Koji Arai Inputs from LIGO Caltech -Inputs from LIGO, Simulation Hiroaki Yamamoto Caltech 4

### Updates from F2F Aug 2016

- Concluded that PRMI by March 2018 is not feasible considering the schedule (<u>JGW-T1605595</u>)
- Output optical tables and related tasks are moved from IOO to MIF
- Sideband frequencies for IMC and ALS decided IMC: 13.78 MHz (<u>JGW-T1605493</u>) ALS: 32 MHz and 33 MHz (<u>JGW-T1605626</u>)
- Some modeling had notable progress OMC suspension ALS NoiseBudget Cryopayload actuator design
- More RF electronics were requested to AEL

#### **bKAGRA** Readiness

Not ready			
	Phase 1	Phase 2	Phase 3
Analog Electronics	requested to AEL	some not yet designed	not yet designed
Digital Control	small modification	major modification	major modification
Initial alignment	planned	vague plan	-
Output tables	some designed	no design	upgrade?
ALS	-	modeling	upgrade?
Modeling	mostly complete	exists but not fully	exists but not fully
OFI / OMC	-	designing	upgrade?