

MIF Subsystem Report

2015/10/8@KAGRA domestic meeting

iKAGRA preparation status

Analog Electronics Preparation

- Circuit production: mostly done
- Electronics layout diagram: done
- Installation
- Cabling
- Tests

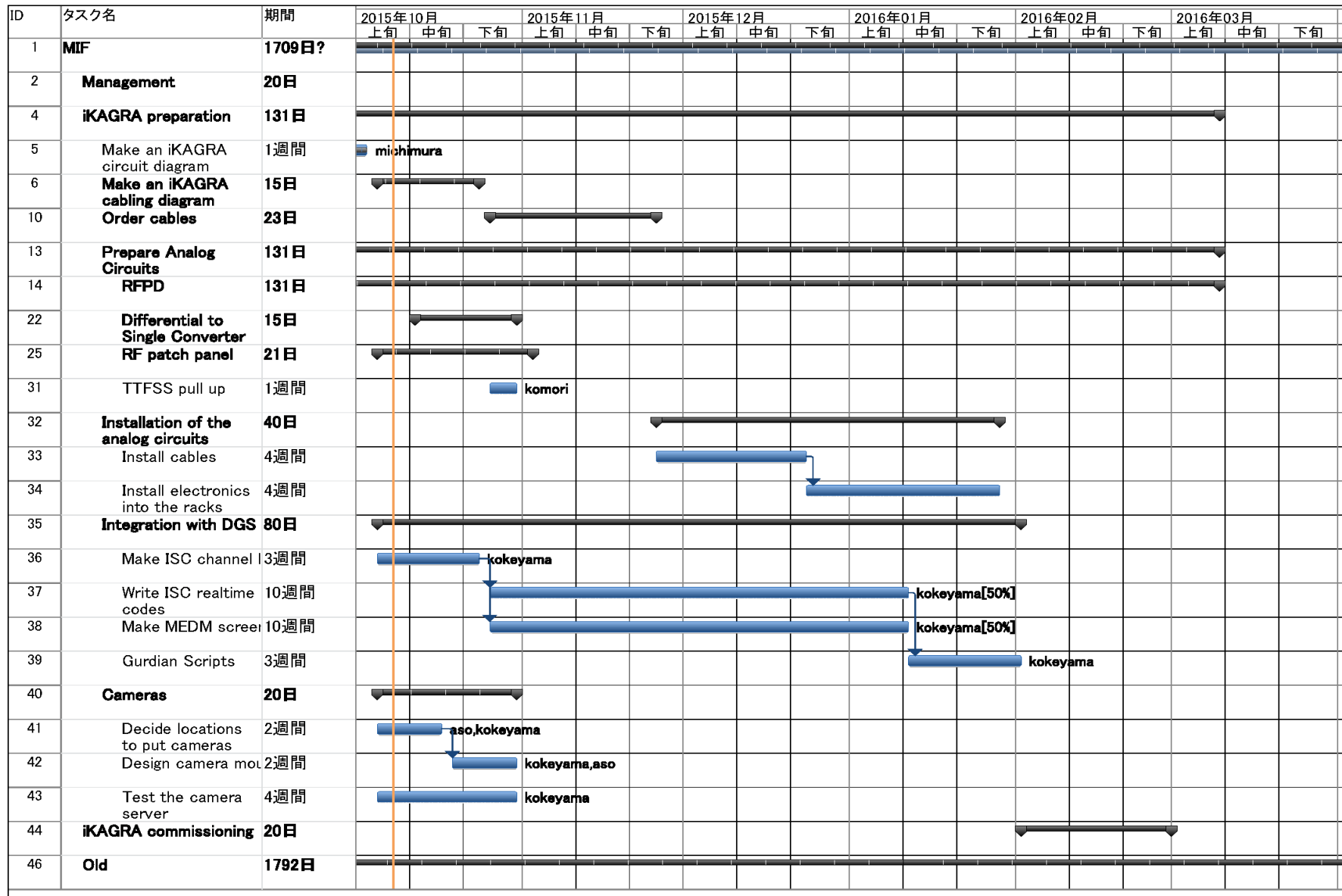
Integration with the digital system

- Channel list up
- Realtime code
- MEDM screens

Commissioning planning

- Alignment procedure
- Schedule

Schedule: preliminary (not discussed yet)



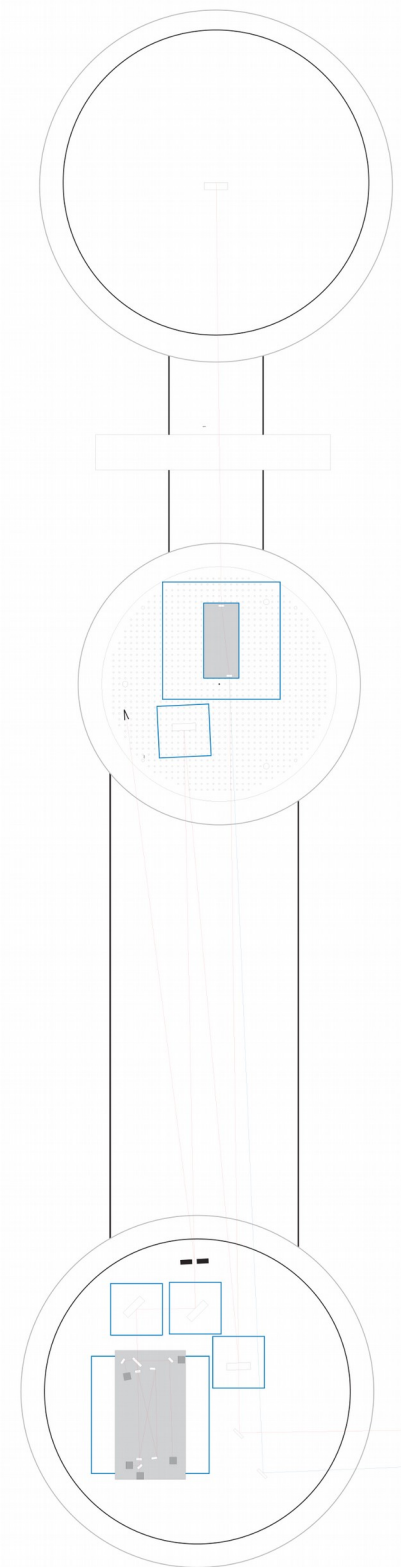
For bKAGRA

FFT simulations of the interferometer to set requirements on the core optics (Hirose, H. Yamamoto)

- Important for determining IBF specification

AS Optics Design (Somiya)

- OFI, OMMT, OMC
- Need to fix the design of the vacuum chambers
- Vibration isolation system for those optics



Instrument control system

- Planning to test it only using the center area module during iKAGRA
- Serious lack of man power
 - Some discussions with cryogenic and detchar people
- Need to move out from the computer room soon with humidity fix

