

T1402527 Request for manufacturing circuits for KAGRA

Date: 24/06/2014

JGWDoc No.: D1402809

• How to apply: Please obtain a document number from JGWDoc with selection of the "Document Type" as "D" like D140XXXX using "Reserve Number" at top menu in each page of JGWDoc. Write down the number you got at "JGWDoc No. " above, fill necessary information blow, and upload this request to JGWDoc with schematic/PCB drawings. Then please tell AEL chief that you have uploaded this request with the document number. Within a week, AEL chief will contact to you to discuss the manufacturing plan of the circuits. If you make the same (or similar with small modifications) circuits you made before, please upload this request as a new version of the same document number. If you have any questions please contact AEL chief.

How to upload to JGWDoc <http://gwwiki.icrr.u-tokyo.ac.jp/JGWwiki/JGWDoc/HowTo/UpLoad>

• Subgroup name : VIS

• Applicant name : Fabián Peña

• Name of the circuit : OSEM readout

• Explanation of function of this circuit:

It provides the current driver for the LED and the transimpedance amplifier of the photodiode

• Schematic attached PCB design attached The last design exists

When you upload this request, please upload a schematic at least, and PCB drawings if you can. If you upload no PCB drawing, AEL can design PCB but it takes more time. We do not accept a conceptual design without schematic. The default CAD used in AEL group is Altium. If you need to use Altium, we can register you as a network user, so please ask AEL chief.

• Shape when delivered. : board only, in a 19inch chassis (1U, 2U, 3U, 4U), other box

• Necessary quantity : 1 (Actual quantity of board and electric parts will be determined by AEL.)

• Delivery date you hope : Please talk with Takahashi-san

It will be determined by discussion between AEL and your group.

Typical term from our past experience

(from submission to deliver): 5 months for a new design, 3 months for existing designs.