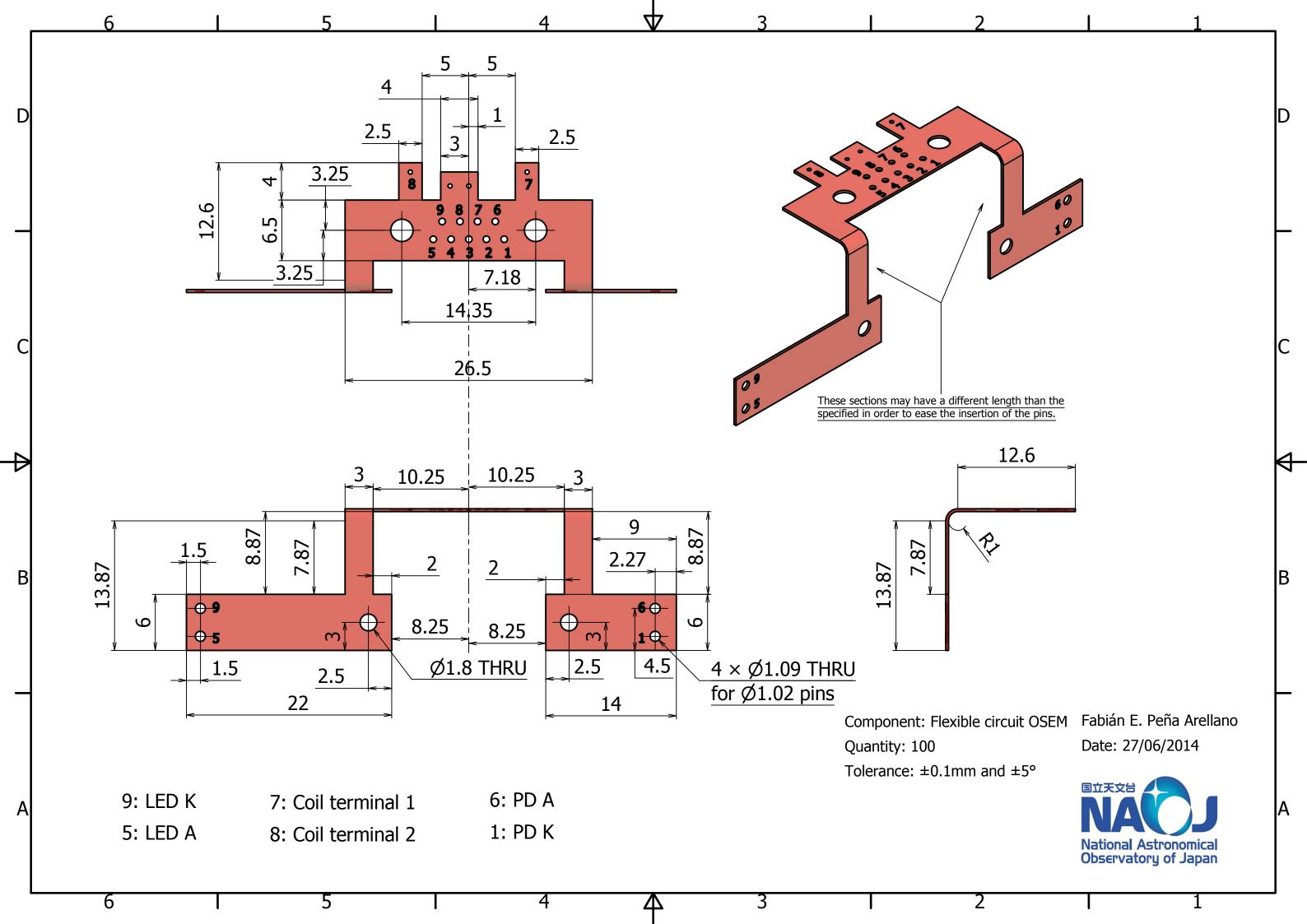
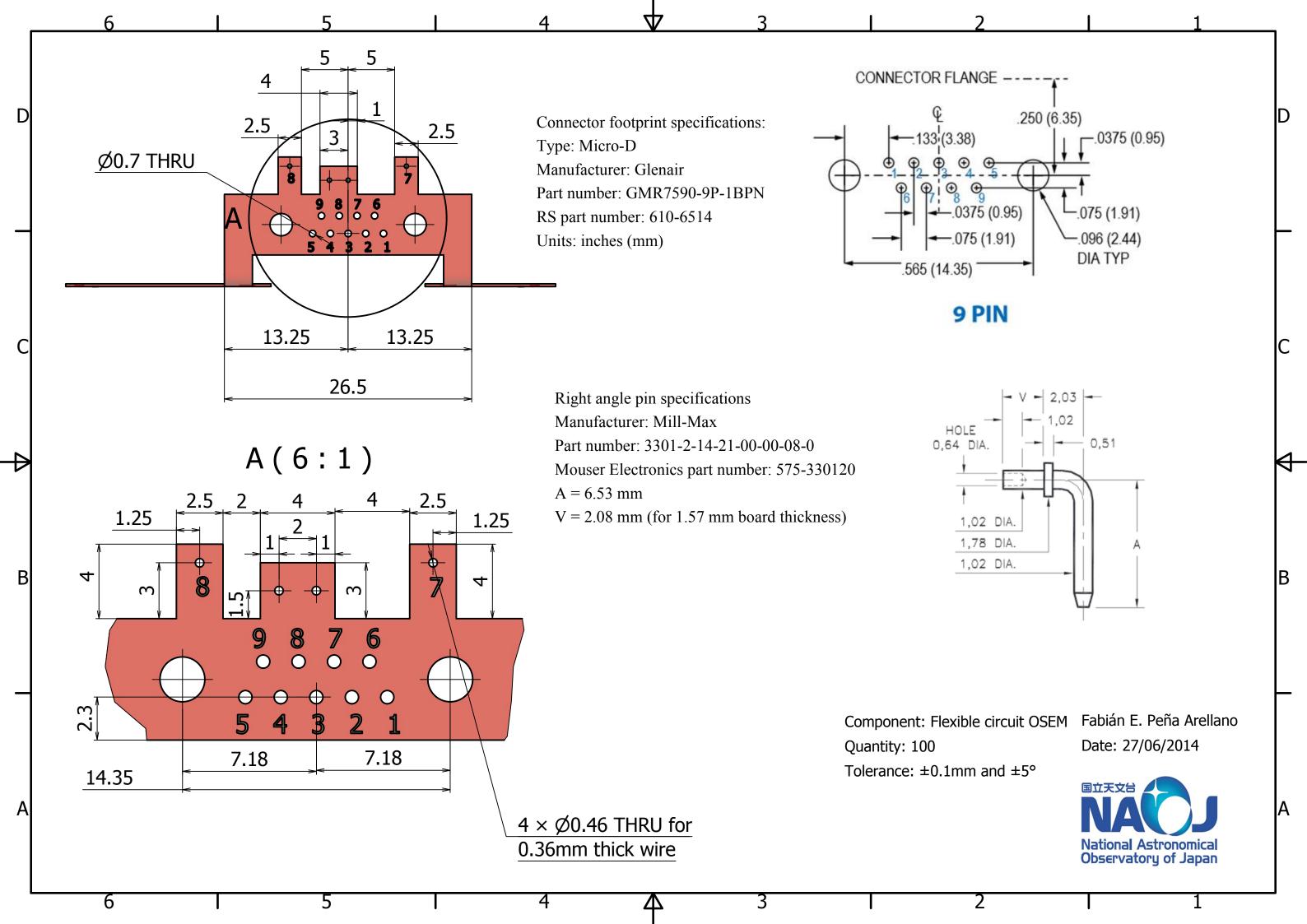
T1402527 Request for manufacturing circuits for KAGRA

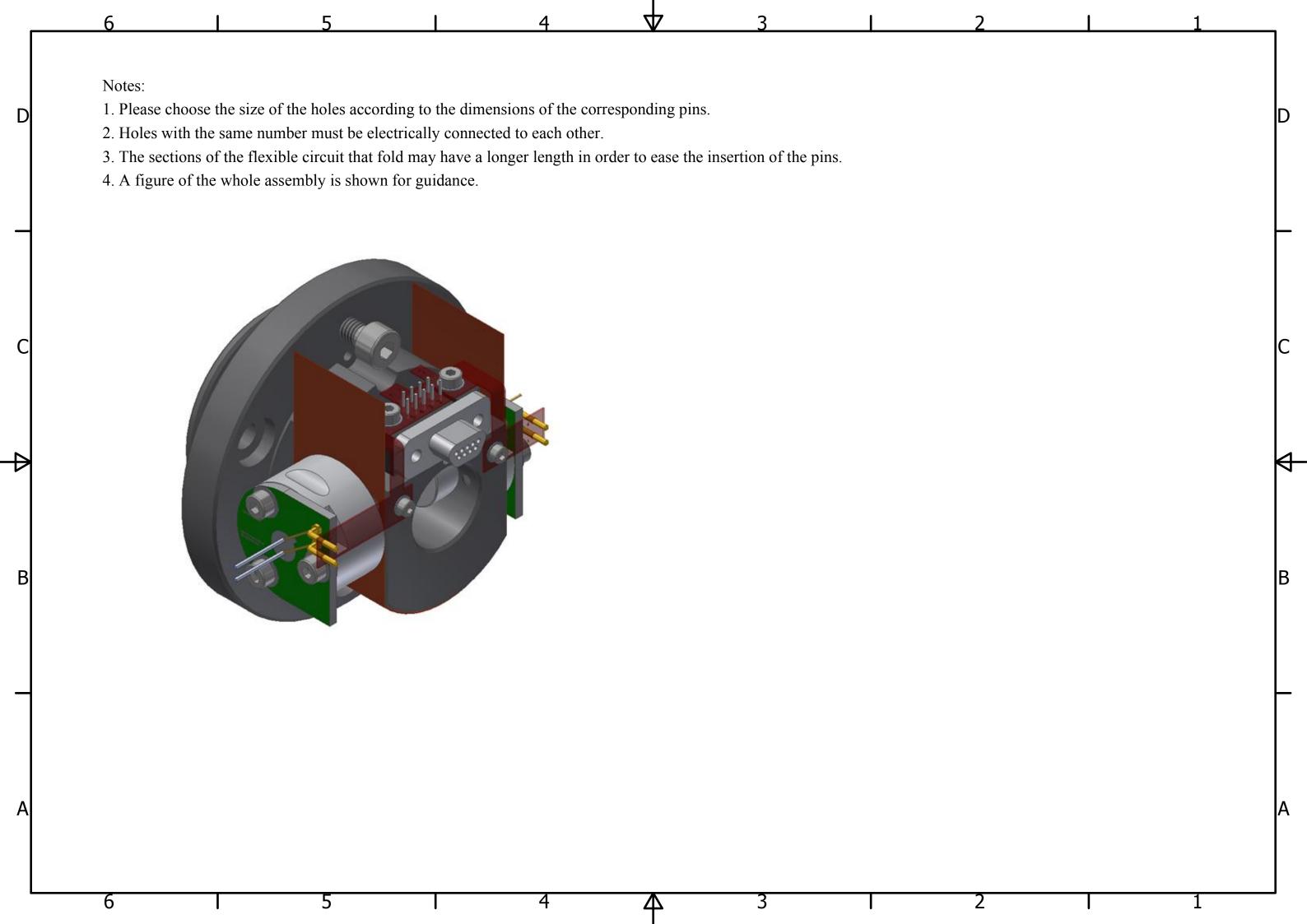
Date: 28/08/2014

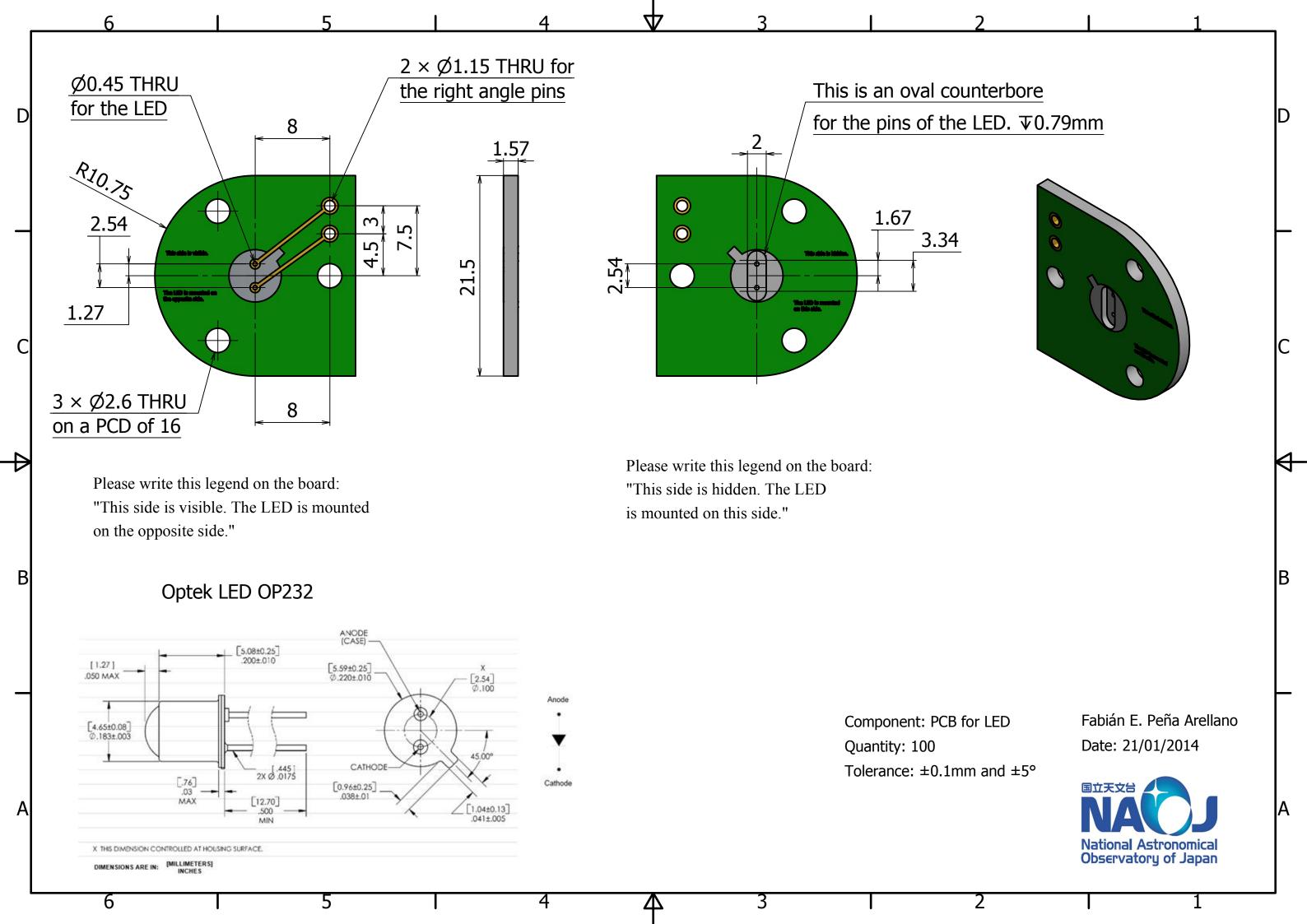
JGWDoc No.: <u>D1402796</u>

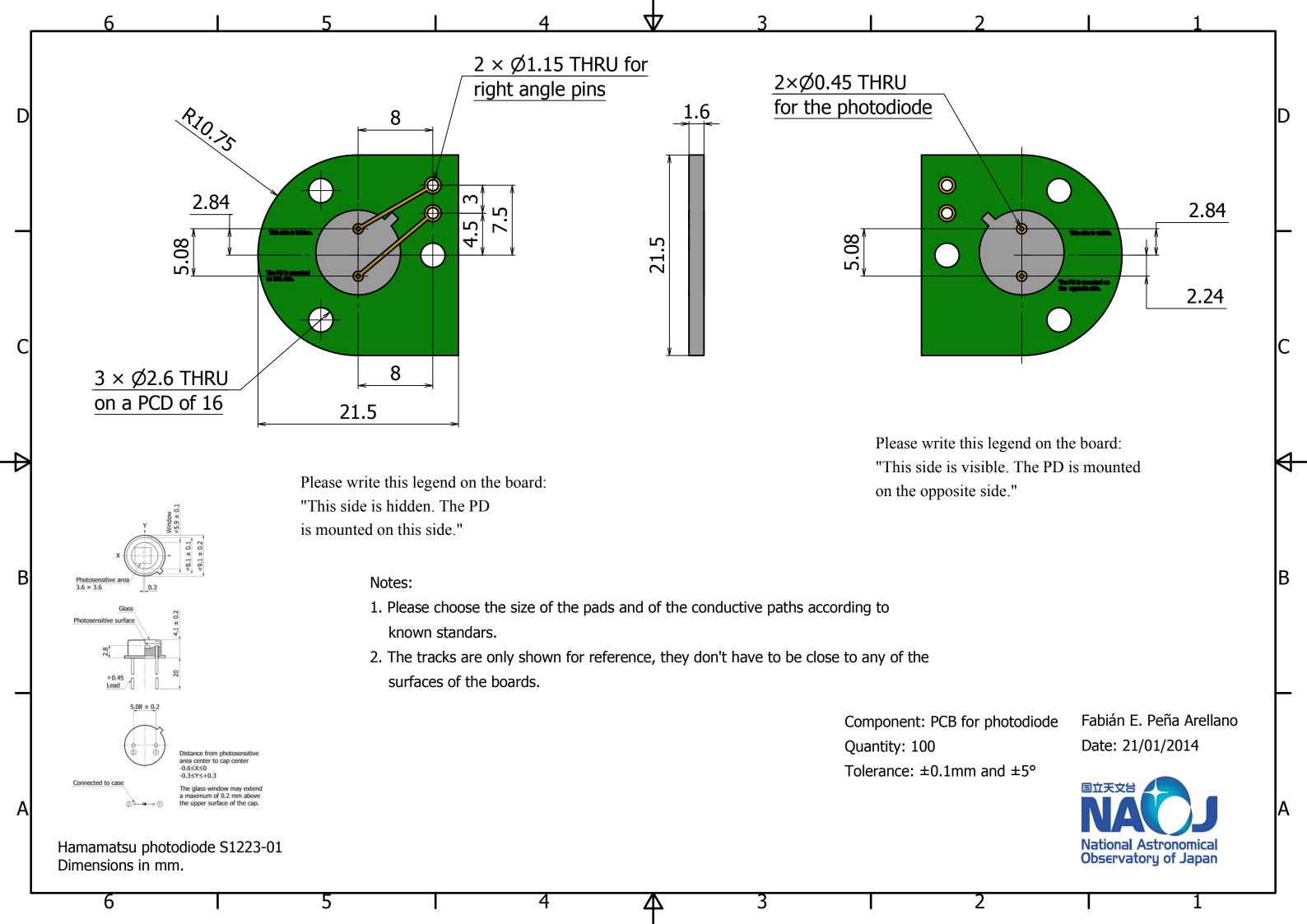
How to apply: Please obtain a document number from JGWDoc with selection of the 'Document Type" as "D" like D140XXXX using "Reserve Number" at top menu in each page of JGWDoc. Write down the number you got at "JGWDoc No." above, fill necessary information blow, and upload this request to JGWDoc with schematic/PCB drawings. Then please tell AEL chief that you have uploaded this request with the document number. Within a week, AEL chief will contact to you to discuss the manufacturing plan of the circuits. If you make the same (or similar with small modifications) circuits you made before, please upload this request as a new version of the same document number. If you have any questions please contact AEL chief. How to upload to JGWDoc http://gwwiki.icrr.u-tokyo.ac.jp/JGWwiki/JGWDoc/HowTo/UpLoad
• Subgroup name : VIS
• Applicant name : Fabián Peña Arellano
Name of the circuit: PCB holders and flexible circuit for OSEMs
• Explanation of function of this circuit:
The PCB holders support the LEDs and the photodiodes in the OSEM.
The flexible circuit provides electric contact between the individual components (LEDs,
photodiodes and coils) and the micro-D connector.
• Schematic attached $oldsymbol{oldsymbol{arphi}}$ PCB design attached $oldsymbol{arphi}$ The last design exists $oldsymbol{arphi}$
When you upload this request, please upload a schematic at least, and PCB drawings if you can. If you upload no PCB drawing, AEL can design PCB but it takes more time. We do not accept a conceptual design without schematic. The default CAD used in AEL group is Altium. If you need to use Altium, we can register you as a network user, so please ask AEL chief.
• Shape when delivered. : \square board only, \varnothing in a 19inch chassis (\varnothing 1U, \varnothing 2U, \varnothing 3U, \varnothing 4U), \varnothing other box
• Necessary quantity : 100 of each (Actual quantity of board and electric
parts will be determined by AEL.)
• Delivery date you hope : Please, talk with Takahashi-san
It will be determined by discussion between AEL and your group.
Typical term from our past experience (from submission to deliver): 5 months for a new
design 3 months for existing designs











Notes

September 18, 2014

- 1. The diagrams are quantitative guidelines and the real PCBs still have to be designed.
- 2. The components must be compatible with ultra-high vacuum. According to the document Printed Circuit Boards for Ultra High Vacuum by Daniel Sigg the following materials should be used:
 - (a) Board: Rogers RO3003 which is a ceramic-filled PTFE composite.
 - (b) Solder: Kester 275 Fluxed-cored wire.
 - (c) DuPont Pyralux Flexible Circuit, which comprises:
 - i. Pyralux LF Copper-Clad Laminate, which is a Kapton (polyimide) film bonded to a copper foil with a C-staged modified acrylic adhesive.
 - ii. Pyralux LF Coverlaycomposite, which is constructed of Kapton polyimide film, coated on one side with a proprietary B-staged modified acrylic adhesive.
- 3. The list above contains hyperlinks.