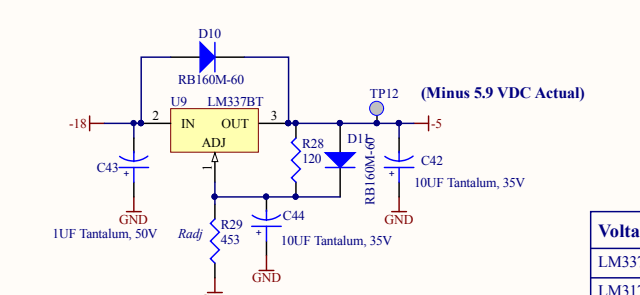
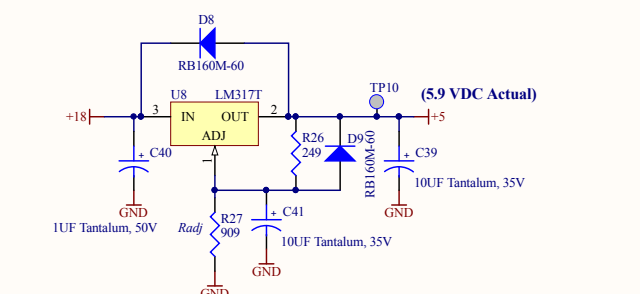
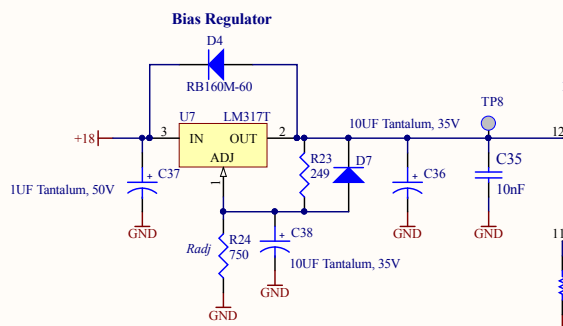
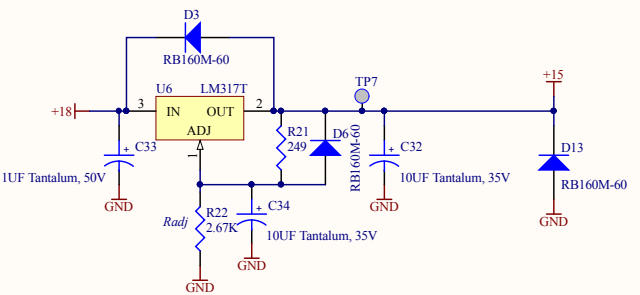
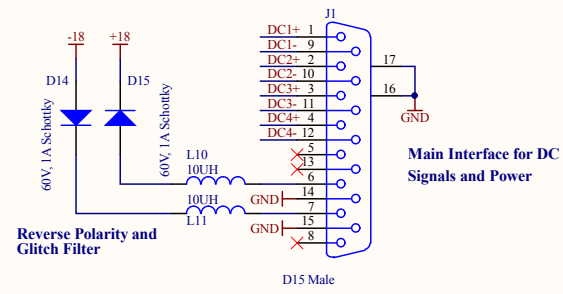
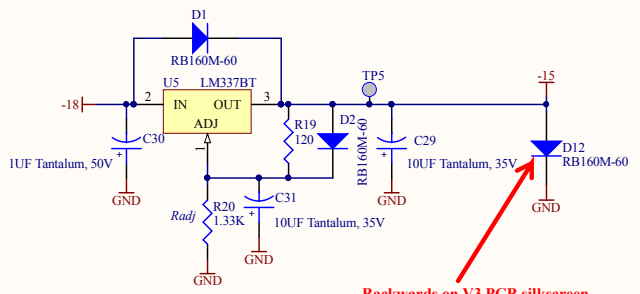
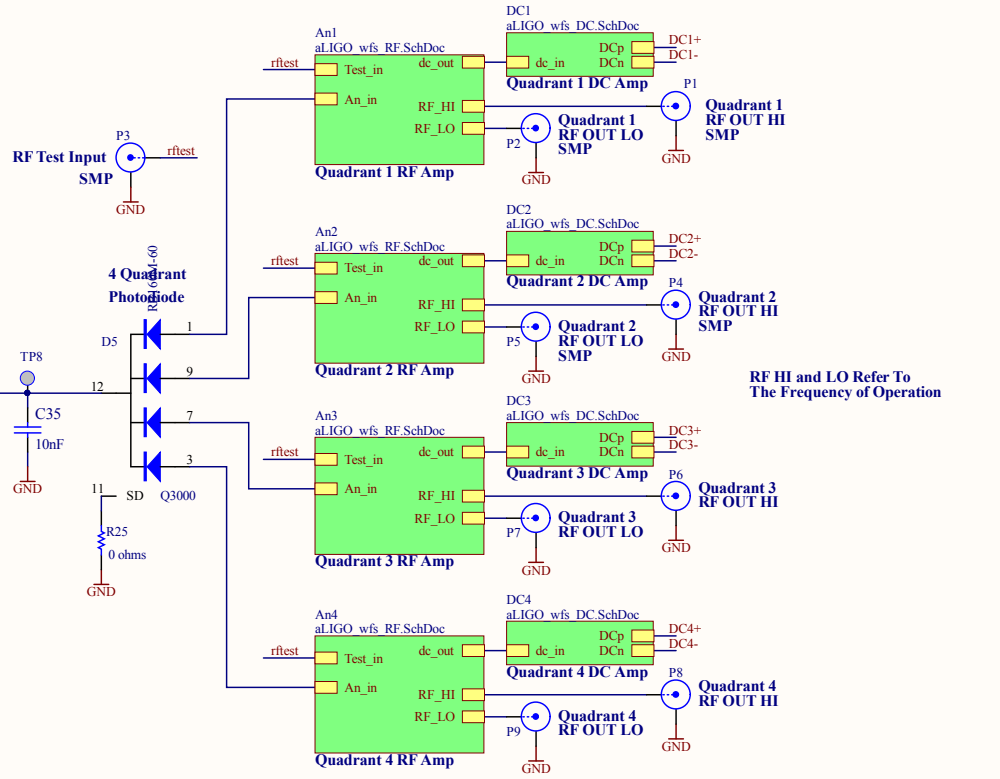
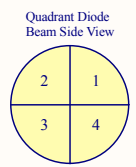


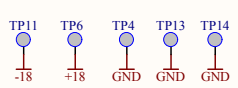
Revision History:
 Rev1 - Initial release
 Rev2 - In response to single supply sensitivity, changed U2 from AD8597 to LT1128, changed U1 from AD8599 to AD8672, removed C28 to stabilize LT1128. Bypassed C29 and C32 with reverse biased 1N4001 diode to clamp the +/-15V regulated supplies.
 Rev3 - PCB updated to include single supply sensitivity fixes. All new orders of this board should use revision 3 schematic and PCB
 Rev4 - Fixed typo in RF component values to reflect actual 9/45MHz RF values. Updated BOM to reflect this change. Resubmitted all files to DCC. V3 PCB still current
 Rev5 - Noticed that D12 had been put in backwards on Rev4. Fixed it in schematic V5, but PCB layout still has backwards footprint indicated in silkscreen. Must revise V3 PCB for next layout (if ever done).



Voltage Regulator Equations	
LM337	$V_o = -1.25(1 + \text{Radj}/120) + (50\mu\text{A} * \text{Radj})$
LM317	$V_o = 1.25(1 + \text{Radj}/249) + (100\mu\text{A} * \text{Radj})$

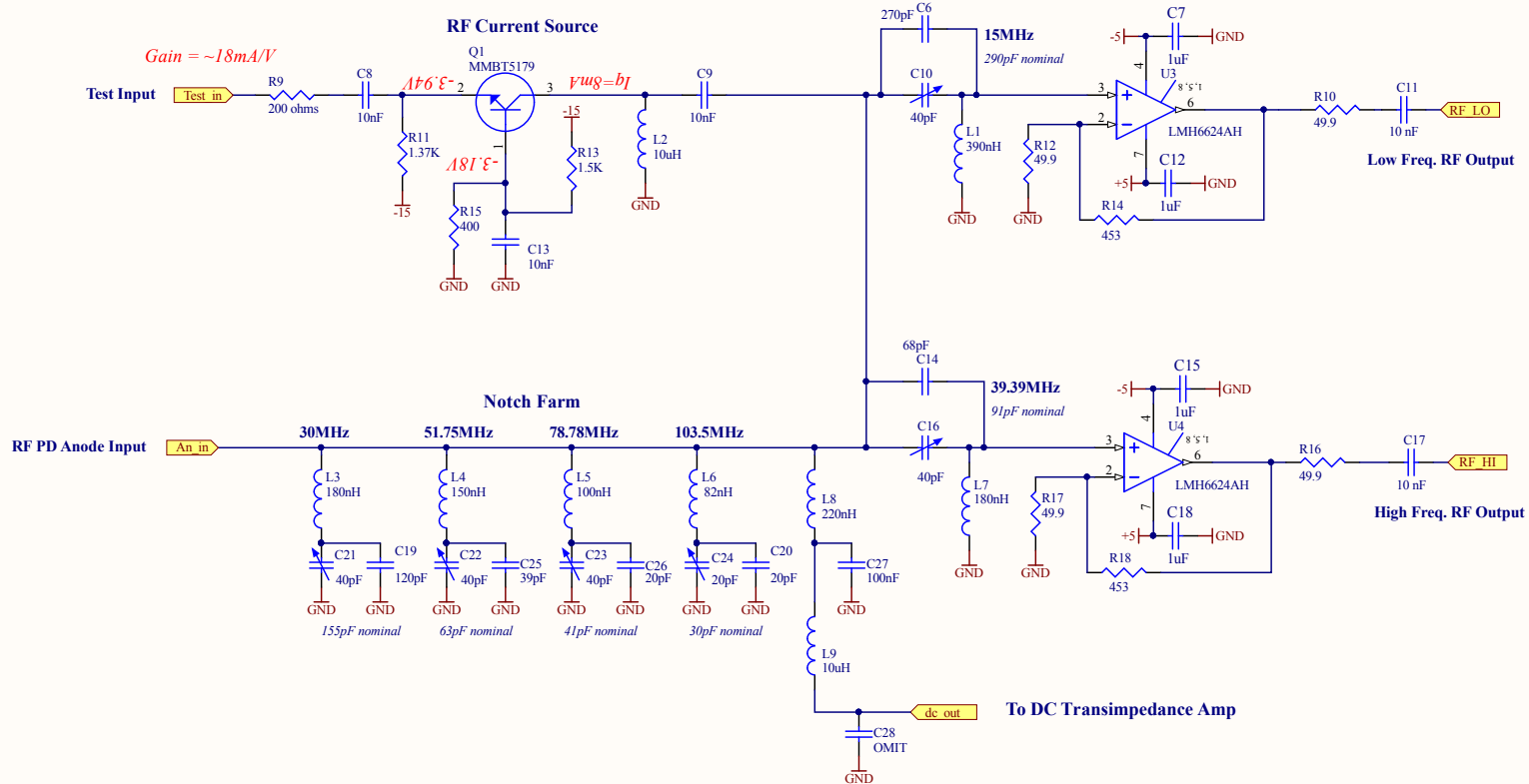


RF HI and LO Refer To The Frequency of Operation



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Values shown for 9/45 MHz. For component variations at different operating frequencies, see T1300199

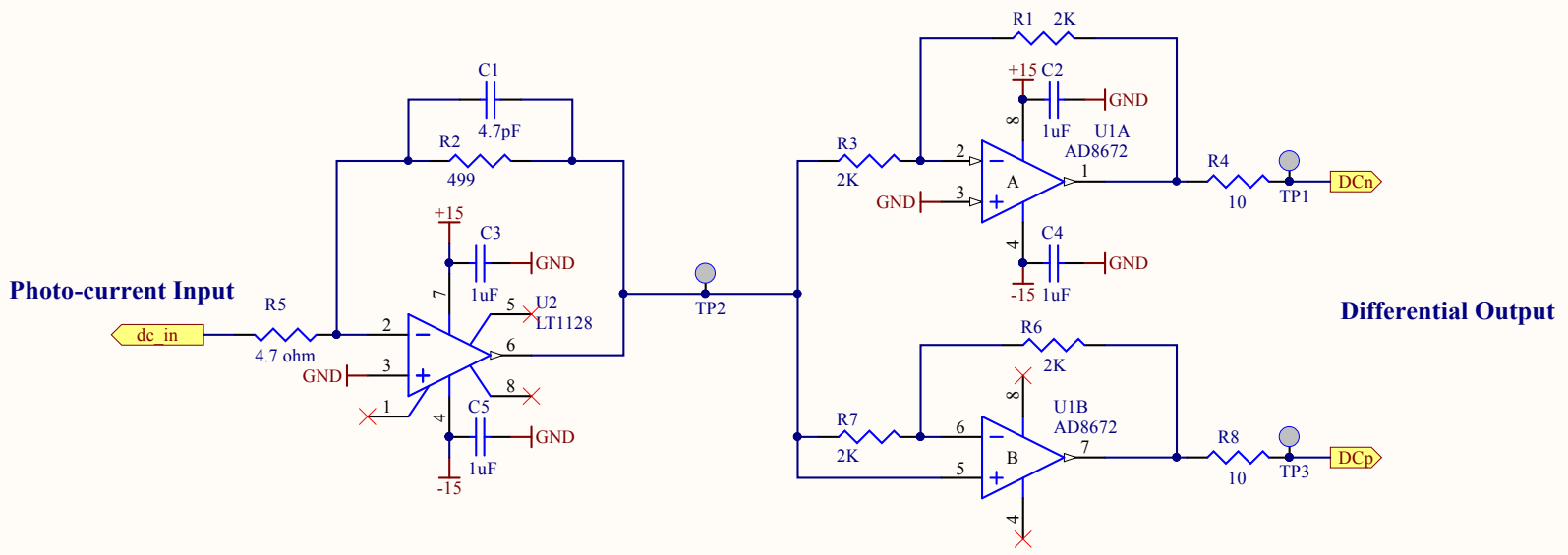


9/45 Design Corresponds to: C:\Rich's Files\LT Spice\PhotodiodeAnalysis\lsc_rfpds\ALIGO_ASC_2011\9_45_ASC_v3.asc

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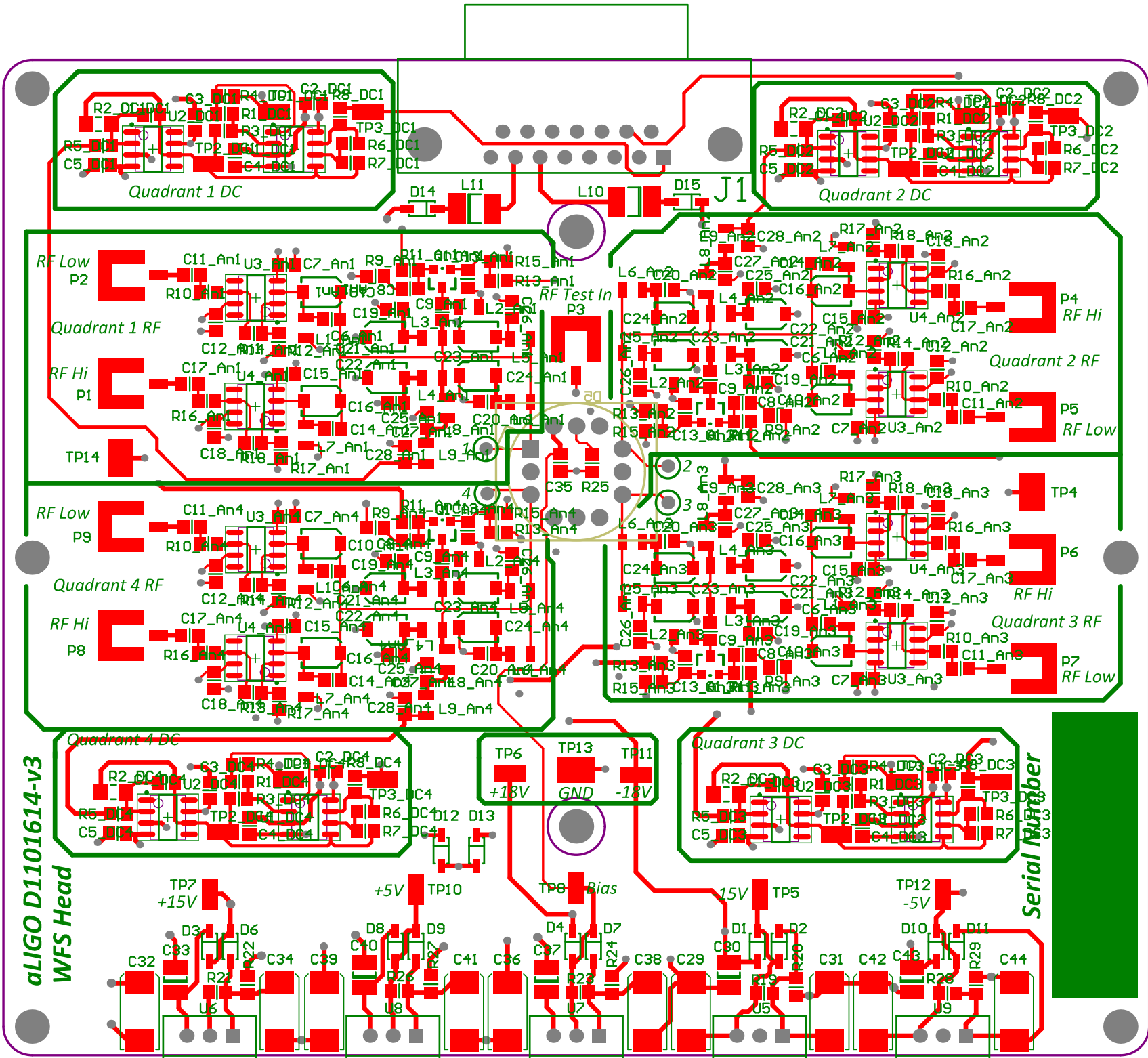
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				Sheet 2 of 3	

DC Transimpedance Amp (15mA Maximum Photo-current)



Last Edited: 29 August 2013

Title DC Section		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: A	DCC Number: D1101614	Revision: v5	Engineer: R. Abbott	Date: 2015/03/10	
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aLIGO D1101614-V3
WFS Head

Serial Number

