

1

2

3

4

A

A

B

B

C

C

D

D

37U

5U

1U

4U

1U

4U

1U

4U

4U

4U

Oscillator for timing
standalone 1, RT PC, SUPERMICRO

IO chassis, OneStopSystems

kami2, RT PC, SUPERMICRO

IO chassis, OneStopSystems
(currently not used)

kami2, RT PC, SUPERMICRO

IO chassis, OneStopSystems

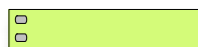
DC Power supply, KIKUSUI 35-10A x 2
(currently not used)

DC Power supply, KIKUSUI 35-10A x 2
(currently not used)

Color Key



Installed



Ready for installation



Mounted but not being used



Waiting for mount

Note: Status as of 8 March 2012
Location: Kamioka IPMU building

Title
Standalone Digital System at Kamioka Rack Layout

Date 2012/3/6 Sheet 1 of 1 Size A3 rev. 0 2012/3/8

Document number
JGW-D1200900 Drawn by
Osamu Miyakawa

1

2

3

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